

Amendments to the Specification:

Amend the paragraph starting at page 7, line 24 to page 8, line 14 as follows:

DI
Sub
EI

The architecture of the present invention can be better
5 depicted in the configuration represented by Figure 2B. The
Connector Interface Element shown at 240 can include a plurality
of processors, at least one of which is used for redundancy
purposes and bus interface cards. An direct memory attached I/O
device such as a Self-Timed Interface bus, hereinafter STI bus
10 (shown at 230) as used in one embodiment of the present
invention, connects the Connector Interface element to the main
storage 110 (also referenced to as the host) which in turn can be
connected to a variety of other network elements and servers
shown at 220 such as web-servers and other TCP/IP oriented
15 servers. The main storage may contain a plurality of host images
110a-110n, each host image for including an application server,
as will be explained. Also as will be explained, main storage
110 includes a plurality of queues and a queuing mechanism. The
Connector Interface Element is in processing communication with
20 the Network Interface Element shown at 260 via another direct
memory attached I/O device such as a Peripheral Controller
Interface bus, hereinafter PCI bus as shown at 250 as used in one
embodiment of the present invention. The I/O device adapters, at
least one or more processors and some local storage reside in the
25 Network Interface Element. Consequently, the Network Interface
Element is connected to individual application users depicted at
270 such as Lotus Notes clients and Web browsers.